## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
51	1	"20050166073"	US-PGPUB; USPAT	OR	OFF	2006/06/15 11:10
52	57	lee-van-h\$.in.	US-PGPUB; USPAT	OR	OFF	2006/06/15 11:11
53	2	S2 and memory adj module\$1	US-PGPUB; USPAT	OR	OFF	2006/06/15 11:13
54	2	S2 and memory near3 (speed\$1 frequenc\$3)	US-PGPUB; USPAT	OR	OFF	2006/06/15 11:14
<i>S5</i>	684	(detect\$3 identif\$4) near3 memory adj module\$1	US-PGPUB; USPAT	OR	OFF	2006/06/15 11:14
56	27	S5 with (speed\$1 frequenc\$3)	US-PGPUB; USPAT	OR	OFF	2006/06/15 11:57
<i>57</i>	2	("2002/0144173").URPN.	USPAT	OR	OFF	2006/06/15 11:21
58	1	("2001/0003198").URPN.	USPAT	OR	OFF	2006/06/15 11:27
517	142	checkstop	US-PGPUB; USPAT	OR	OFF	2006/06/15 12:08
<i>518</i>	18	checkstop	IBM_TDB	OR	OFF	2006/06/15 12:07
519	3	S18 and multiprocessor	IBM_TDB	OR	OFF	2006/06/15 12:01
<i>520</i>	45	S17 and (multiprocessor (dual multi) adj processor)	US-PGPUB; USPAT	OR	OFF	2006/06/15 12:09
521	130	spin adj loop	US-PGPUB; USPAT	OR	OFF	2006/06/15 12:08
522	<i>75</i>	S21 and (multiprocessor (dual multi) adj processor)	US-PGPUB; USPAT	OR	OFF	2006/06/15 13:47
523	3	("5867658").URPN.	USPAT	OR	OFF	2006/06/15 12:22
524	11	("4335426"   "5257378"   "5287511"   "5444850"   "5499352"   "5524263"   "5548776"   "5548796"   "5590292"   "5590351"   "5729675").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/06/15 12:24
542	112	(chang\$3 switch\$3) near3 (frequenc\$3 speed\$1) same (multiprocessor (dual multi) adj processor)	US-PGPUB; USPAT	OR	OFF	2006/06/15 13:49
543	1	("6643792").URPN.	USPAT	OR	OFF	2006/06/15 14:07
<i>544</i>	244	common adj2 clock adj (frequency speed)	US-PGPUB; USPAT	OR	OFF	2006/09/12 17:29
<i>545</i>	31	S44 same register\$1	US-PGPUB; USPAT	OR	OFF	2006/09/12 17:29
546	3025	system adj clock with register\$1	US-PGPUB; USPAT	OR	OFF	2006/09/12 17:38
547	350	S46.clm.	US-PGPUB; USPAT	OR	OFF	2006/09/12 17:34
548	3685	symmetric near2 (multiprocess\$3 multi\$3 adj process\$3)	US-PGPUB; USPAT	OR	OFF	2006/09/12 17:35
549	10	S48 and S46	US-PGPUB; USPAT	OR	OFF	2006/09/12 17:35
<i>550</i>	34998	(system core chipset northbridge) near clock	US-PGPUB; USPAT	OR	OFF	2006/09/12 17:41
<i>551</i>	614	S50 near (configur\$5 set\$4)	US-PGPUB; USPAT	OR	OFF	2006/09/12 17:42
552	16	S51 with register\$1	US-PGPUB; USPAT	OR	OFF	2006/09/12 17:42
<i>553</i>	96	S51.clm.	US-PGPUB; USPAT	OR	OFF	2006/09/12 17:43